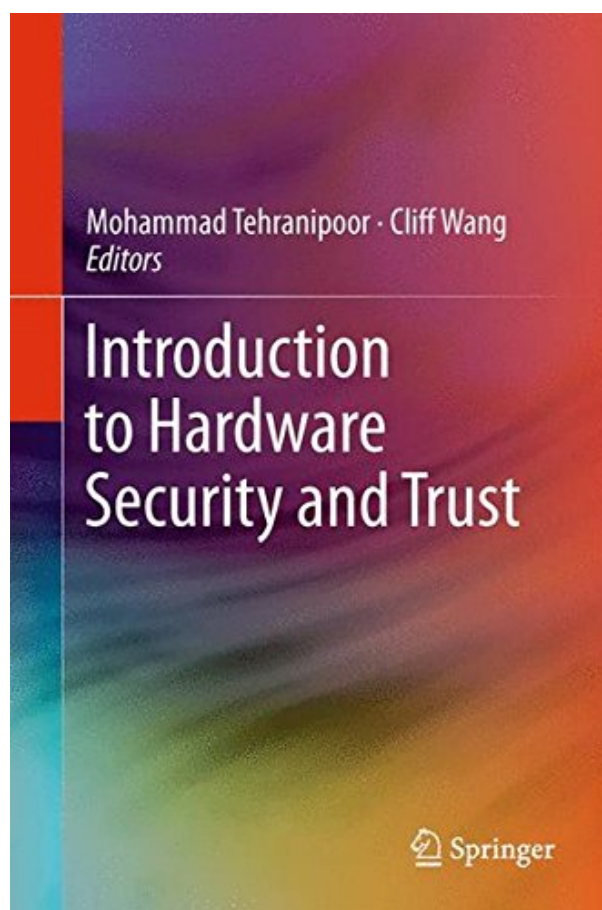
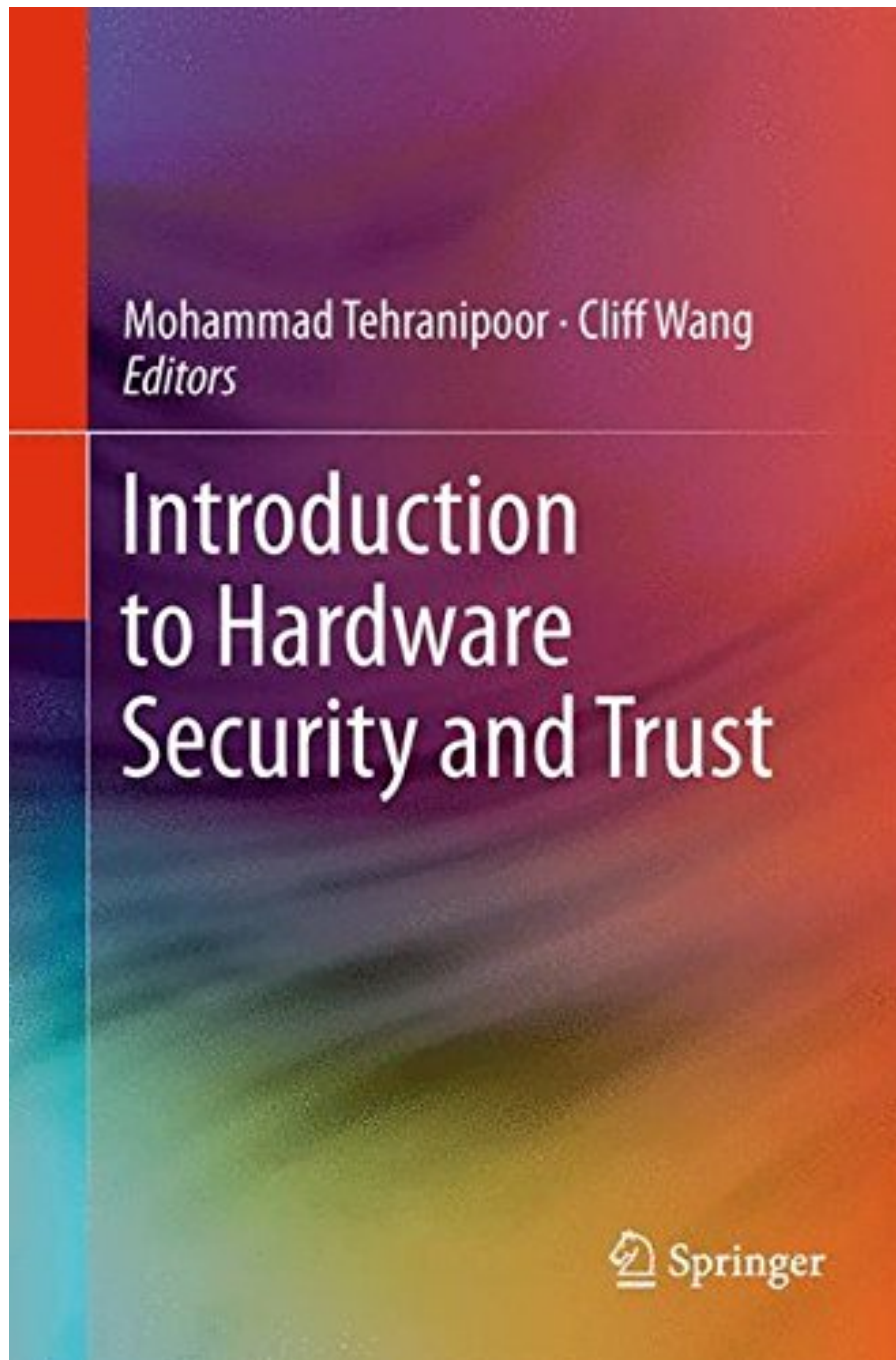


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## Review

From the reviews:

“Editors Tehranipoor and Wand help fill this important gap with their book on the security of electronic devices and systems ... . The text begins with an introduction to very-large-scale integration (VLSI) testing and hardware implementation of hash functions. ... Overall, this book represents an important contribution to the cyber security body of knowledge, providing a comprehensive introduction to hardware security and trust. I consider it highly relevant and recommend it.” (Alessandro Berni, ACM Computing Reviews, June, 2012)

## From the Back Cover

The emergence of a globalized, horizontal semiconductor business model raises a set of concerns involving the security and trust of the information systems on which modern society is increasingly reliant for mission-critical functionality. Hardware-oriented security and trust issues span a broad range including threats related to the malicious insertion of Trojan circuits designed, e.g., to act as a ‘kill switch’ to disable a chip, to integrated circuit (IC) piracy, and to attacks designed to extract encryption keys and IP from a chip.

This book provides the foundations for understanding hardware security and trust, which have become major concerns for national security over the past decade. Coverage includes security and trust issues in all types of electronic devices and systems such as ASICs, COTS, FPGAs, microprocessors/DSPs, and embedded systems. This serves as an invaluable reference to the state-of-the-art research that is of critical significance to the security of, and trust in, modern society’s microelectronic-supported infrastructures.

- Provides a comprehensive introduction to hardware security and trust;
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- Describes a variety of state-of-the-art applications, such as physically unclonable functions, unclonable RFID tags and attack and countermeasures for smart cards.

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